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L Number	Hits	Search Text	DB	Time stamp
1	2	6628836.pn.	USPAT;	2004/10/14 09:54
			DERWENT	
2	4311	graphic\$4 with compress\$4	USPAT;	2004/10/14 09:54
			DERWENT	3337,237,21 03.01
3	2	(graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 09:57
	_	near variable)	DERWENT	2501,20,11 03.3,
4	4	(graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 09:58
		with variable)	DERWENT	2001, 20, 11 03.50
5	224	·	USPAT;	2004/10/14 09:58
		variable)	DERWENT	2001/10/11 03.30
6	1	((graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 09:59
		and variable)) and (eliminat\$4 with (field	DERWENT	2001/10/14 03.03
		near type))		
7	0	((graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 10:00
		and variable)) and ((delet\$4 or remov\$4)	DERWENT	2001, 20, 11 10.00
		with (field near type))		
8	28	((graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 10:00
		and variable)) and (field near type)	DERWENT	
9	22	(((graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 10:00
		and variable)) and (field near type)) and	DERWENT	
		packet\$4		
10	2	((((graphic\$4 with compress\$4) and (setup	USPAT;	2004/10/14 10:02
	1	and variable)) and (field near type)) and	DERWENT	
		packet\$4) and geometr\$4	İ	
11	62	duluk.in.	USPAT;	2004/10/14 10:02
			DERWENT	
12	0	duluk,?.in.	USPAT;	2004/10/14 10:02
			DERWENT	
13	62	duluk,.in.	USPAT;	2004/10/14 10:03
			DERWENT	
14	0	duluk,.in. and ((((graphic\$4 with	USPAT;	2004/10/14 10:03
		compress\$4) and (setup and variable)) and	DERWENT	
		(field near type)) and packet\$4)		
15	0	duluk, .in. and (graphic\$4 with compress\$4)	USPAT;	2004/10/14 10:03
			DERWENT	

	ט	1	Document	Issue Date	Page s	Title	Current
1			US 6628836 B1	20030930	11	Sort middle, screen space, graphics geometry compression through redundancy elimination	382/232
2			US 6615338 B1	20030902	32	Clustered architecture in a VLIW processor	712/24
3			US 6279100 B1	20010821	48	Local stall control method and structure in a microprocessor	712/24
4			US 6628836 B	20030930	11	Computer graphics compression method involves modifying setup variables identified when all slope values have zero values, by eliminating slope values from variable set	

	Current XRef	Retrieva l Classif	Inventor	S	С	P	2	3	4	5
1 1	345/501; 345/506	!	Wittenbrink, Craig M. et al.	⊠						
2	712/217		Tremblay, Marc et al.	×						
3	712/219	;	Tremblay, Marc et al.	⊠						
4			ORDENTLICH, E et	×						

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1	US 6628836	
2	US 6615338	
3	US 6279100	
4	US 6628836	

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	υ	1	Document	Issue Date	Page s	Title	Current
1	×		US 6229553 B1	20010508	23	Deferred shading graphics pipeline processor	345/506
2			US 6229553 B	20010508		Deferred graphic pipeline processor for three-dimension computer graphics system, has mode injection unit that maintains cache status information	
3	\boxtimes		WO 200011607 A	20000302		Three-dimensional graphics processor implementing differed shading and other enhanced features during computer generation of pictures, images or other graphical or pictorial information	

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	Current	Retrieva l Classif	Inventor	S	С	P	2	3	4	5
1	345/419; 345/426; 345/582; 345/584		Duluk, Jr., Jerome F. et al.							
2		1	ARNOLD, V T et al.							
3		·	ARNOLD, V T et al.							

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1	US 6229553	
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